

LOGICAL CIRCUIT DESIGN APPARATUS 50

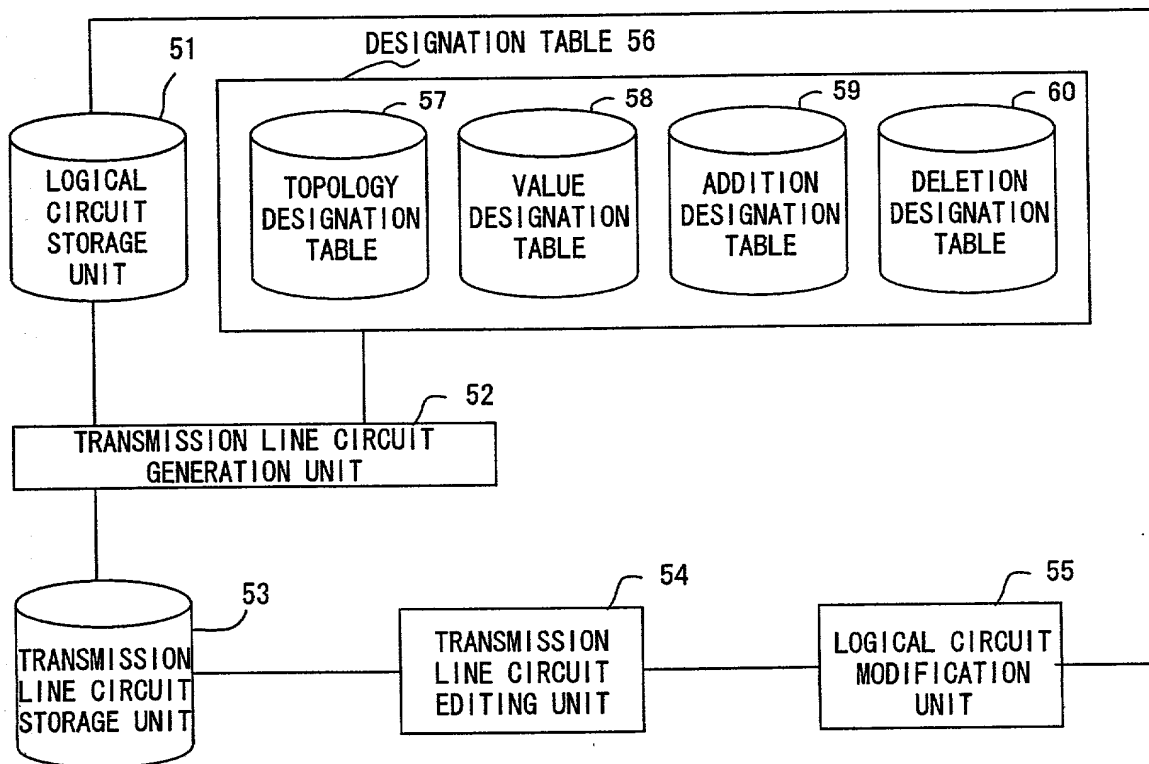


FIG. 1

441630-103860

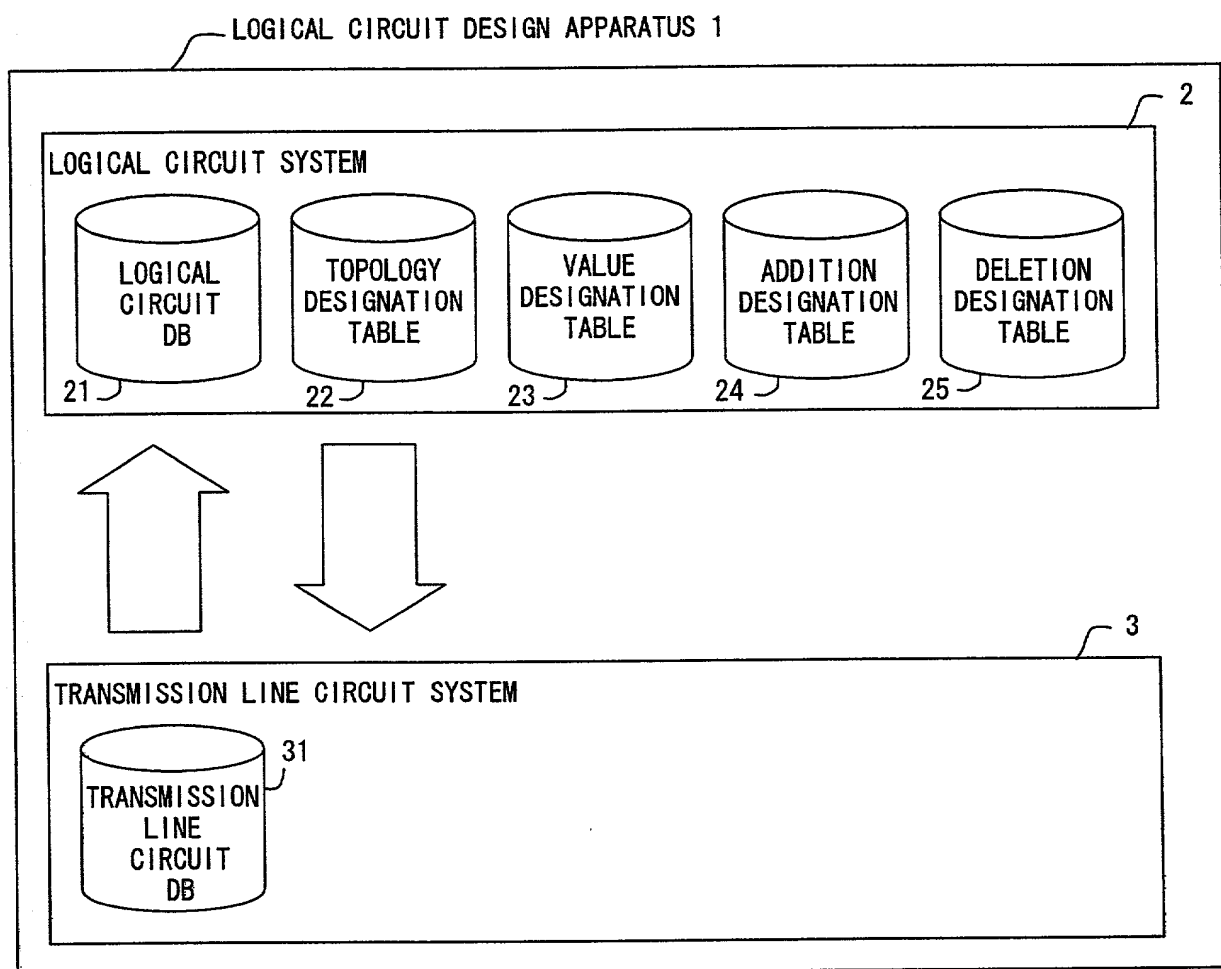


FIG. 2

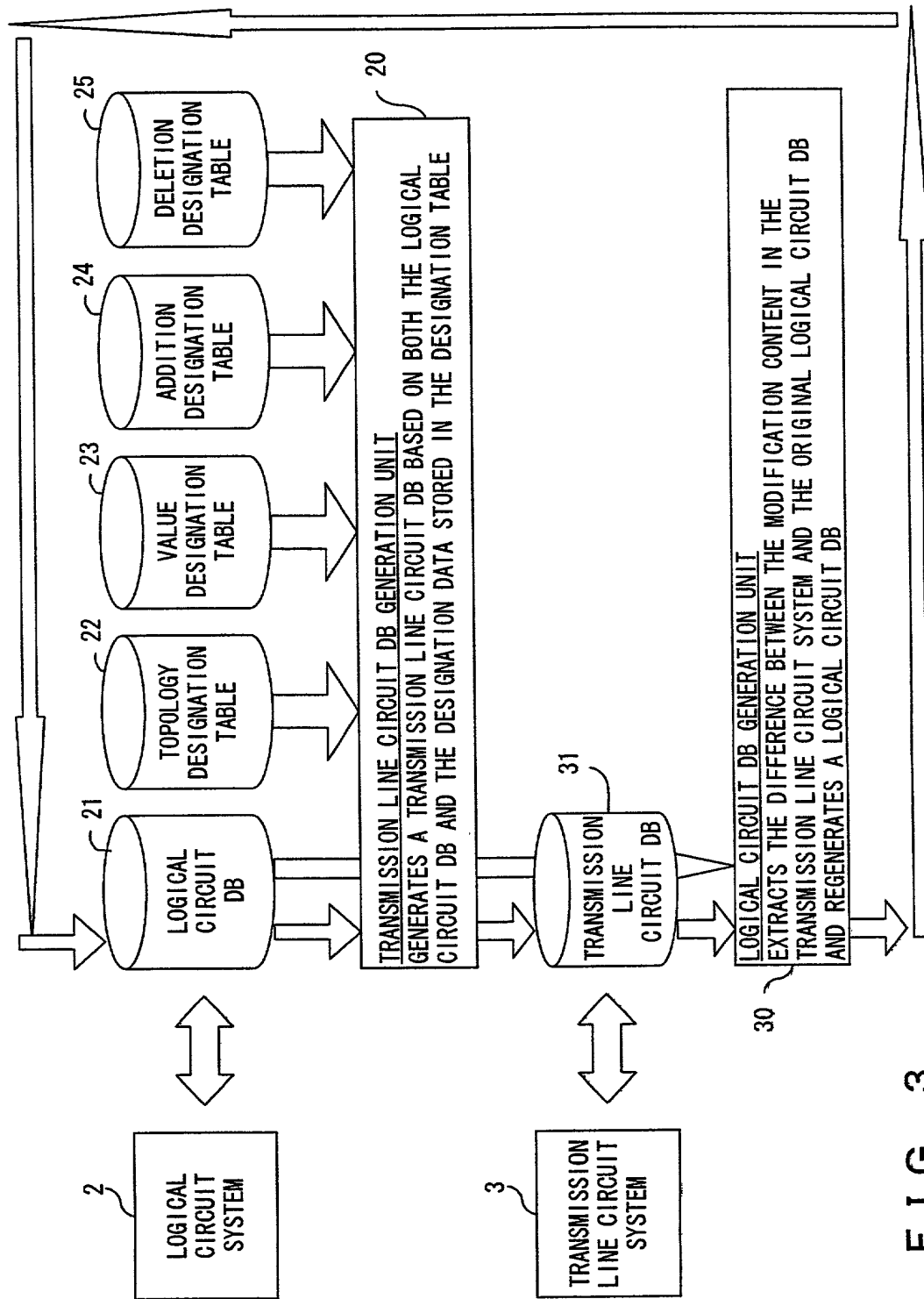
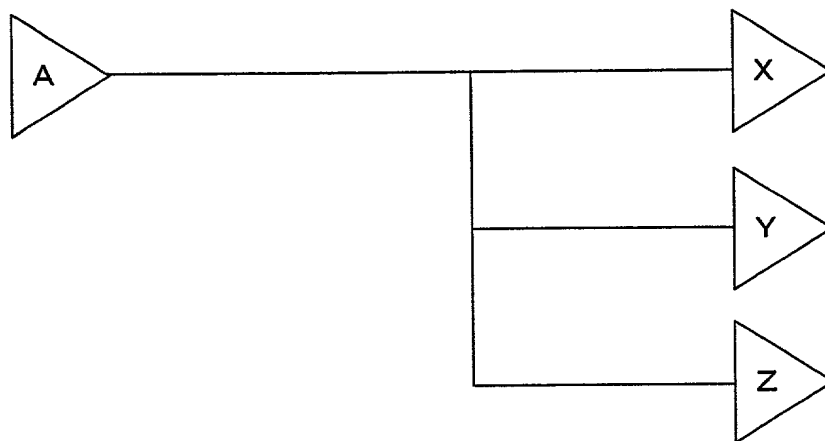
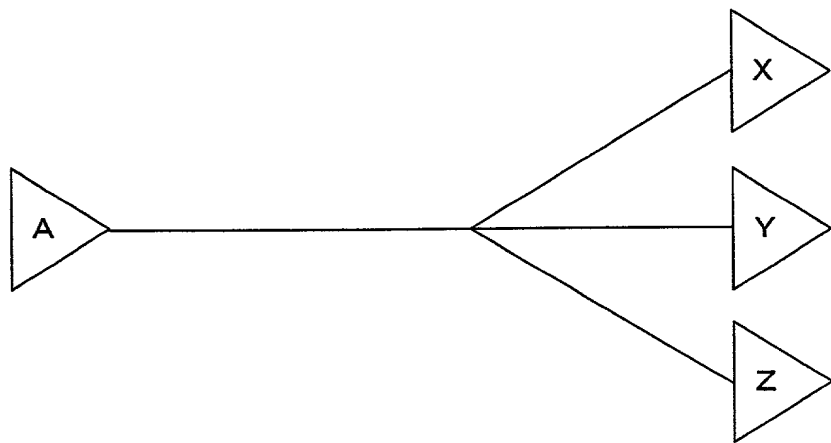


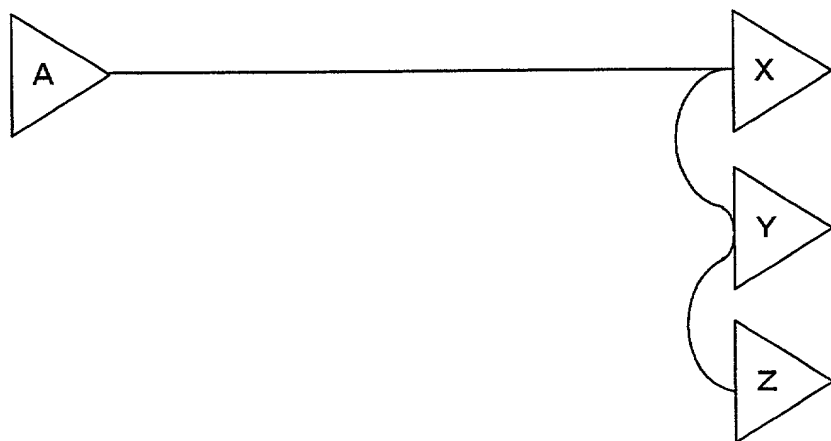
FIG. 3



F I G. 4

FIG. 5





F I G. 6

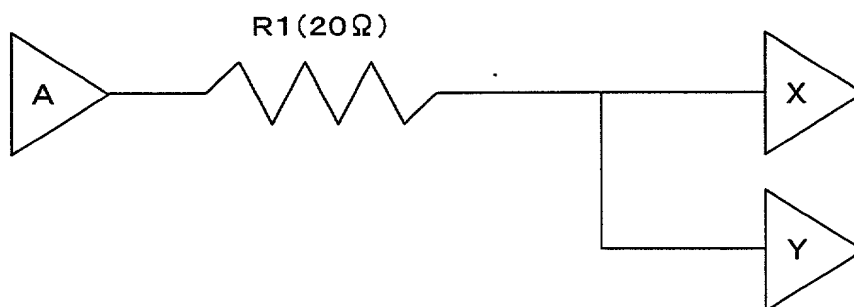


FIG. 7

ITEM NO.	SYMBOL	COMPONENT DESCRIPTION	VALUE
1	R1	RESISTOR	33 Ω

FIG. 8

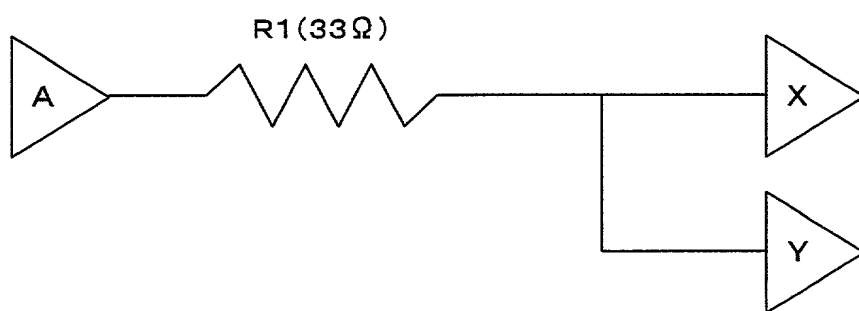


FIG. 9

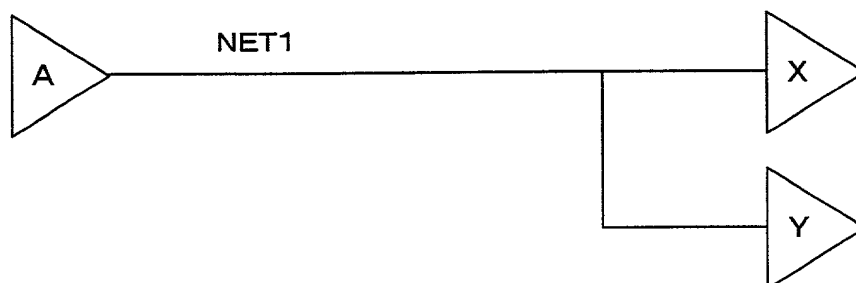


FIG. 10

6050-1050560

ITEM NO.	NET	LOCATION	COMPONENT DESCRIPTION	VALUE
1	NET1	BY THE SIDE OF A	RESISTOR	33Ω

FIG. 11

FIG. 12

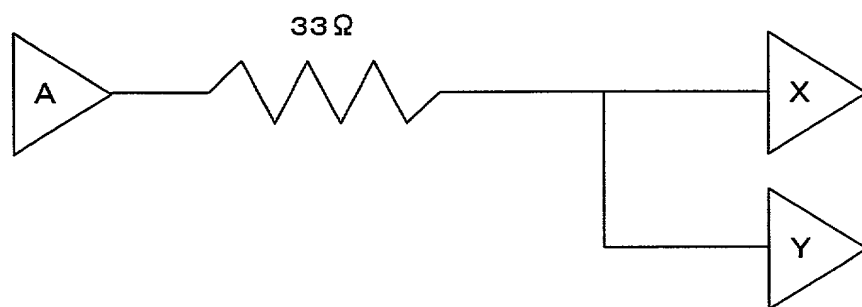


FIG. 12

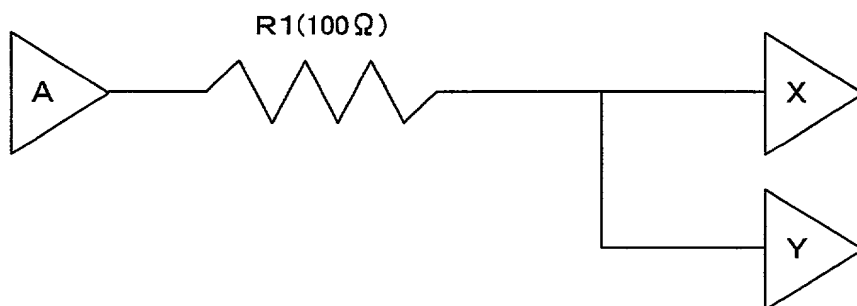
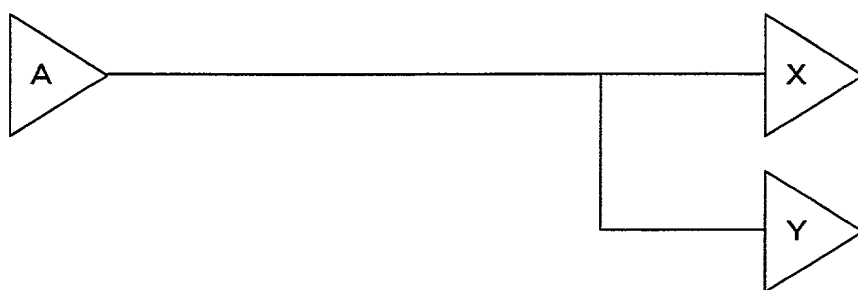


FIG. 13

ITEM NO.	SYMBOL
1	R1

F I G. 1 4

FIG. 15



R1(20Ω)

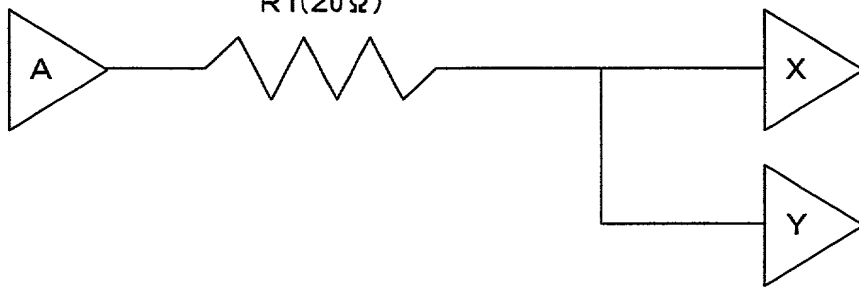


FIG. 16

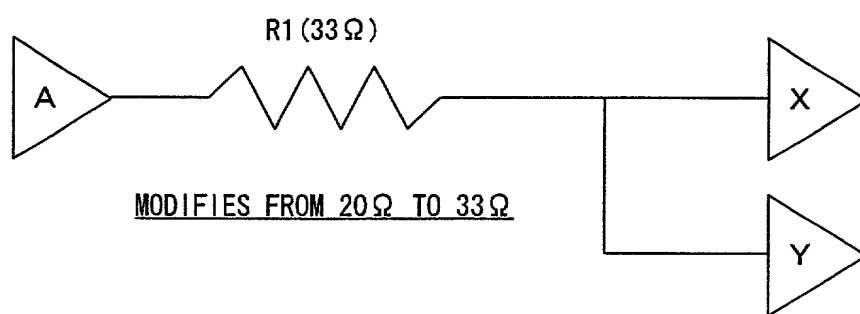


FIG. 17

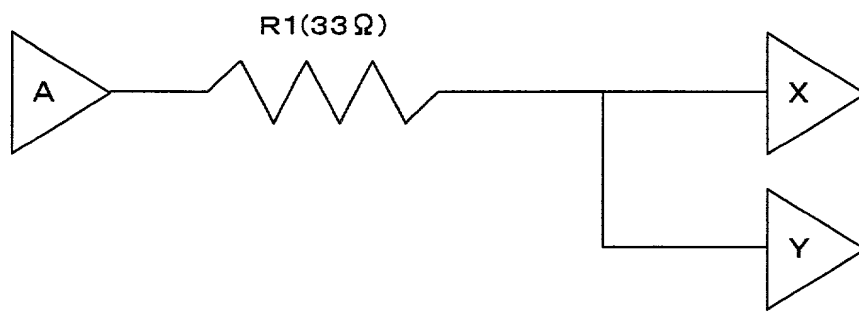


FIG. 18

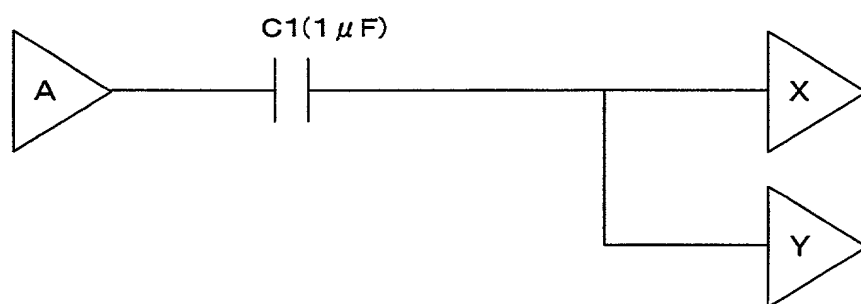


FIG. 19

FIG. 20

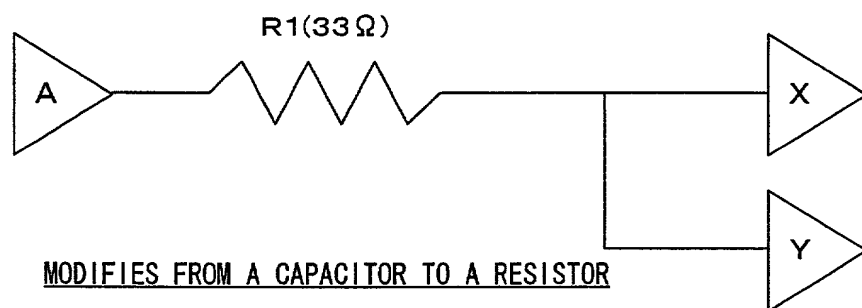


FIG. 20

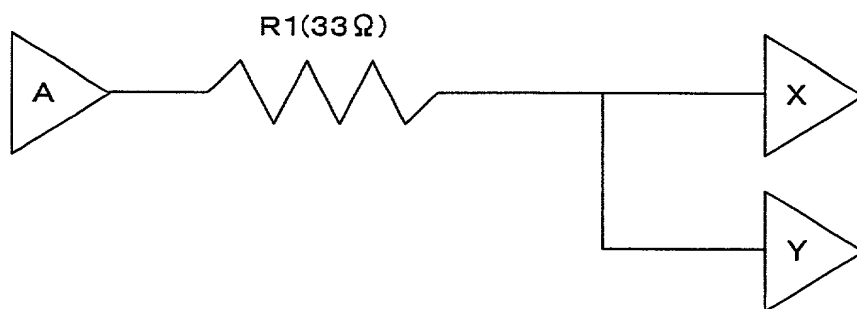


FIG. 21

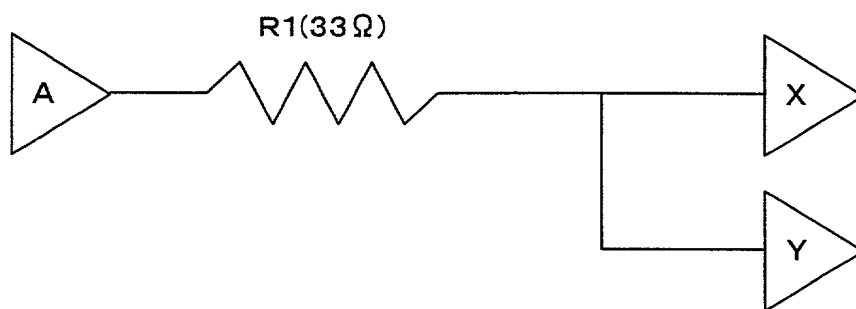


FIG. 22

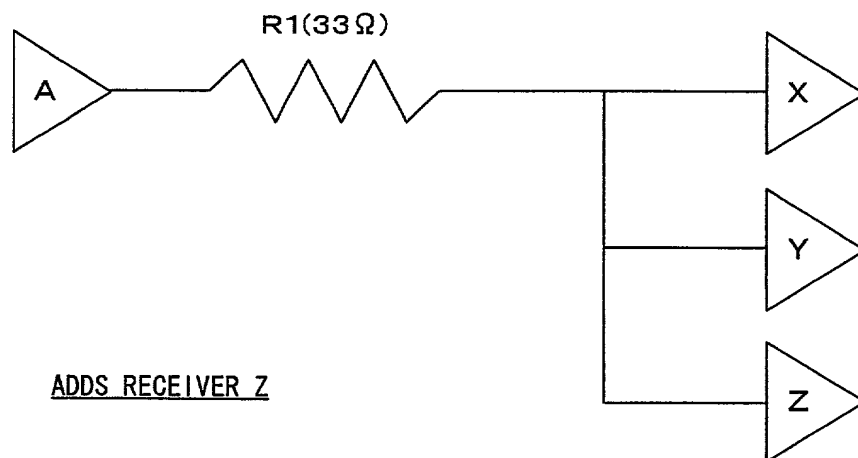


FIG. 23

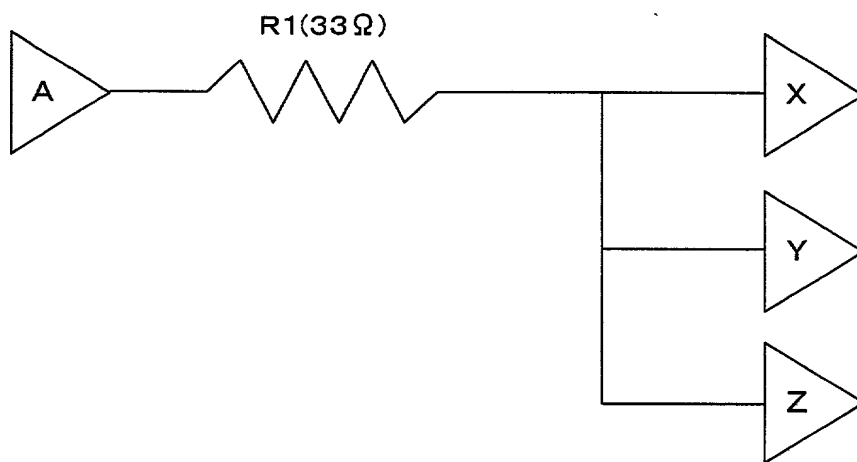


FIG. 24

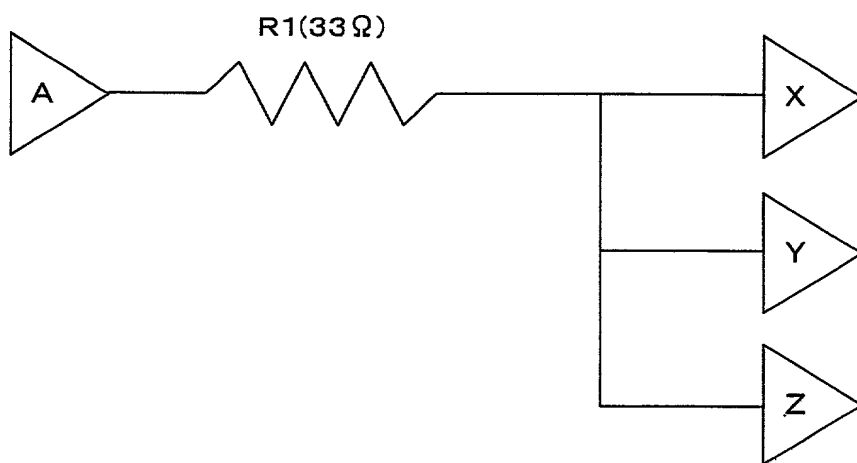


FIG. 25

FIG. 26

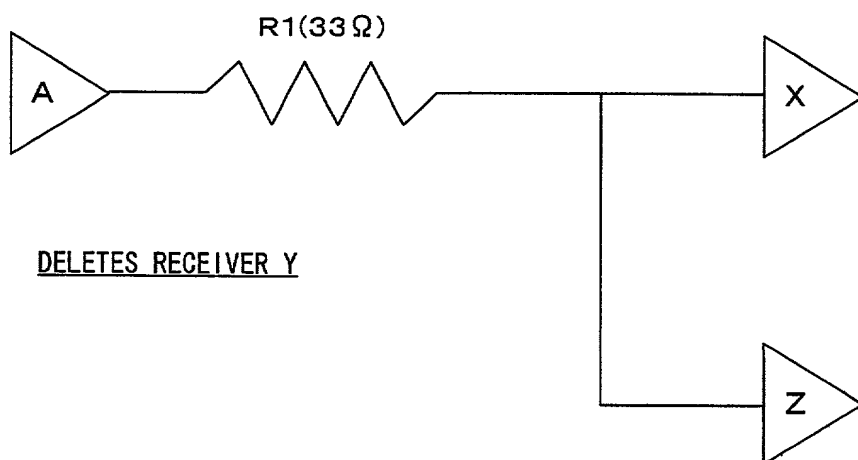


FIG. 26

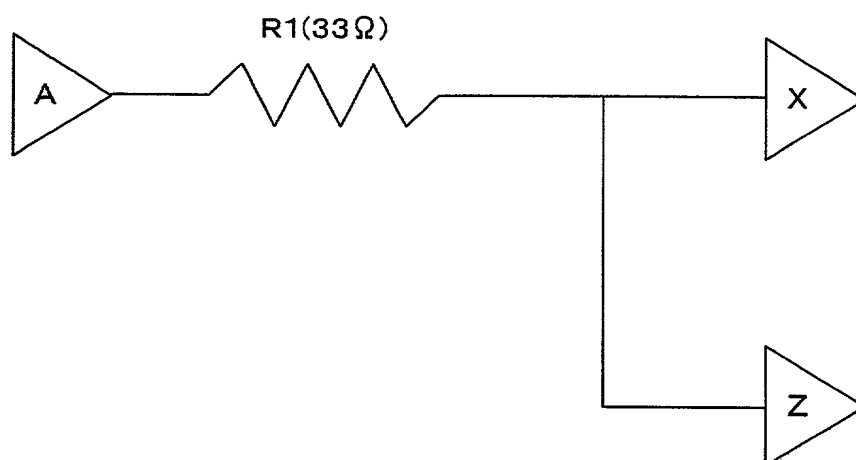


FIG. 27

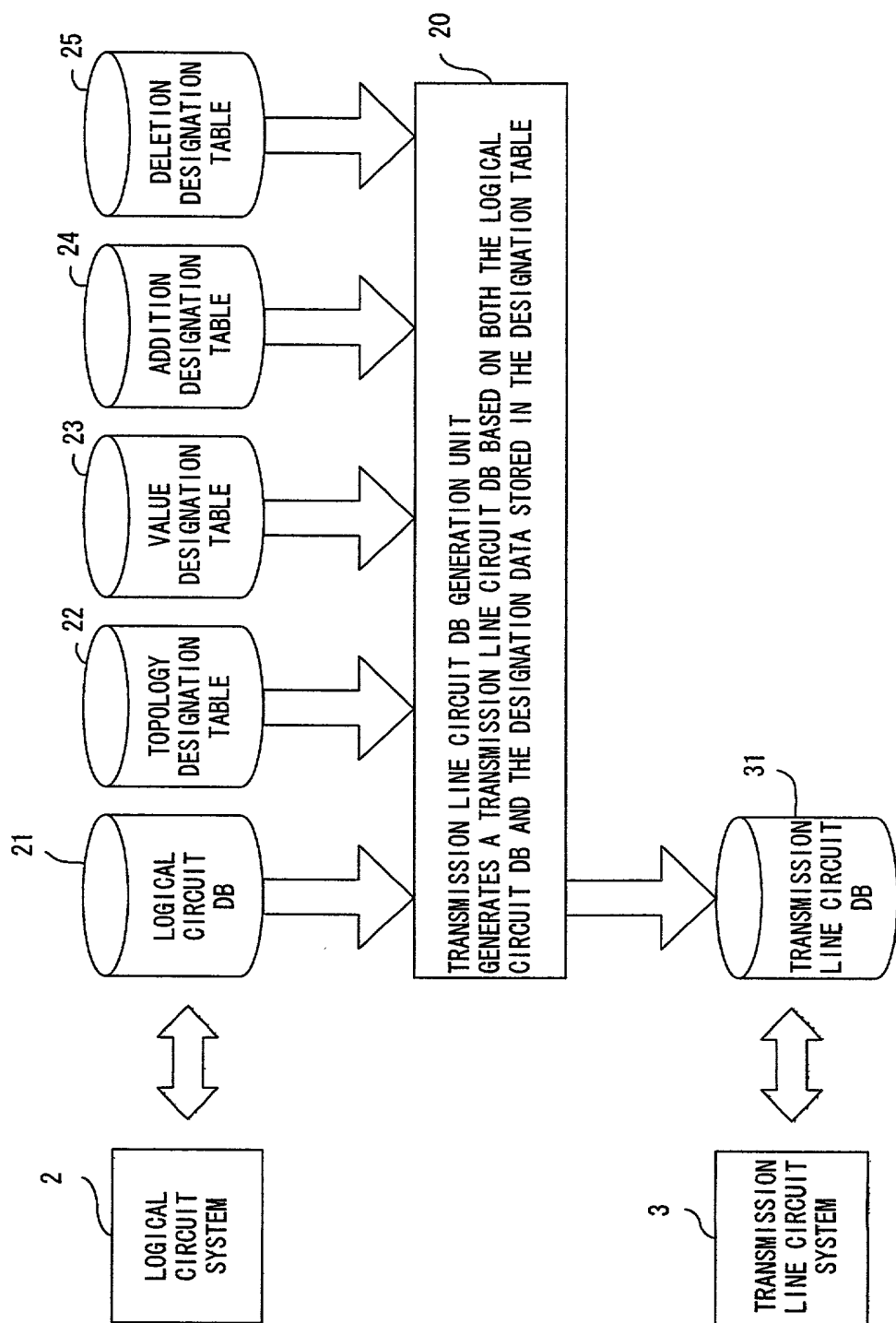


FIG. 28

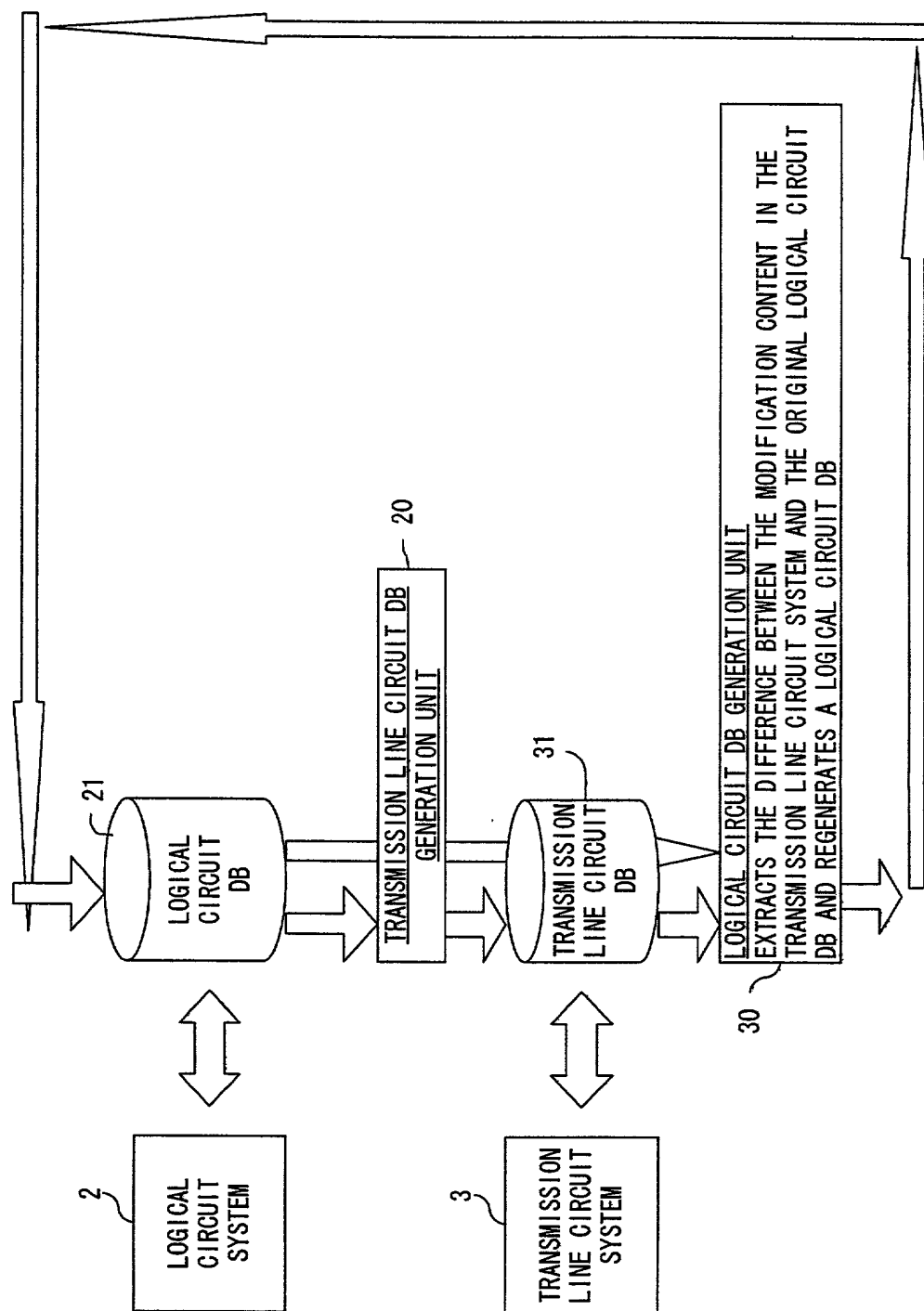


FIG. 29

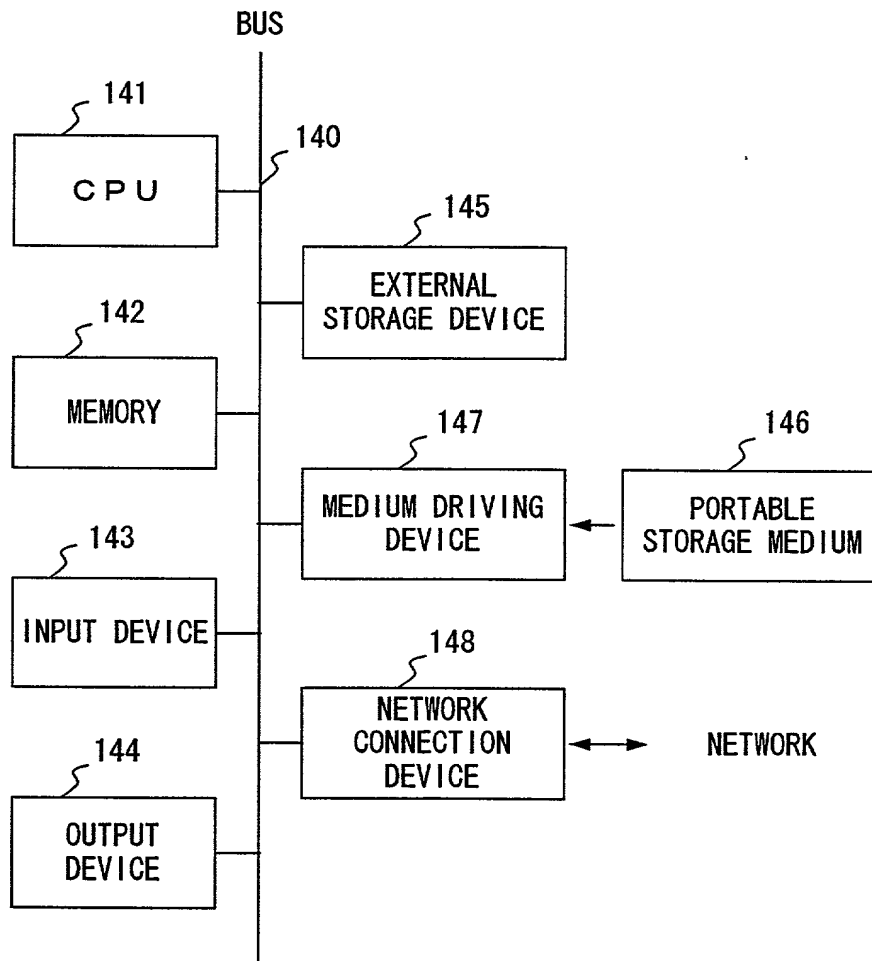


FIG. 30

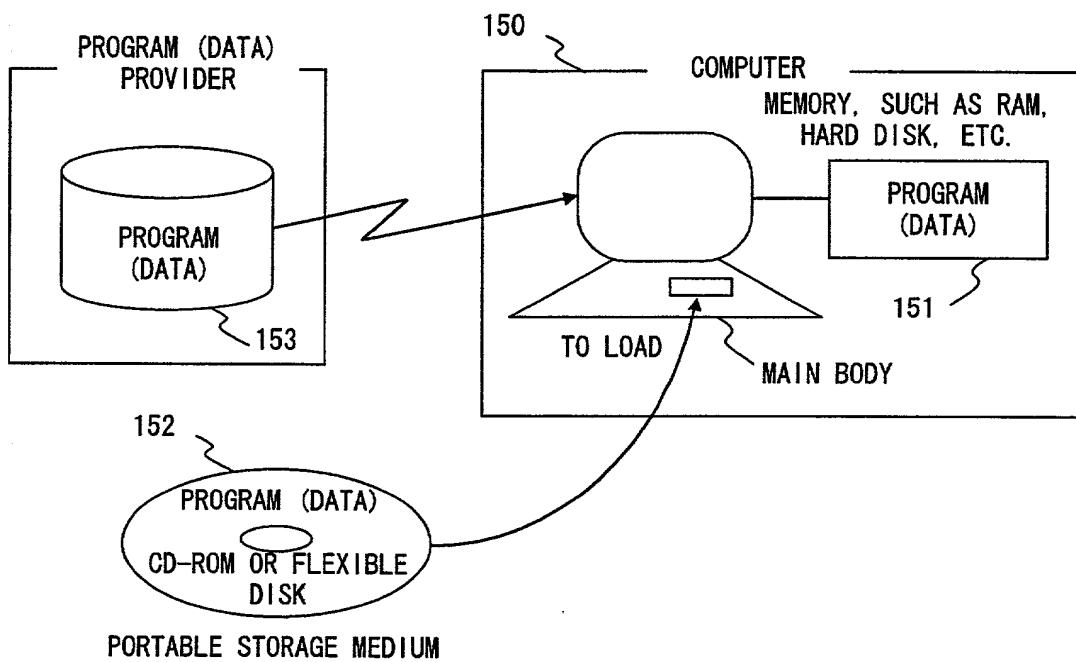


FIG. 31